

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig. 1

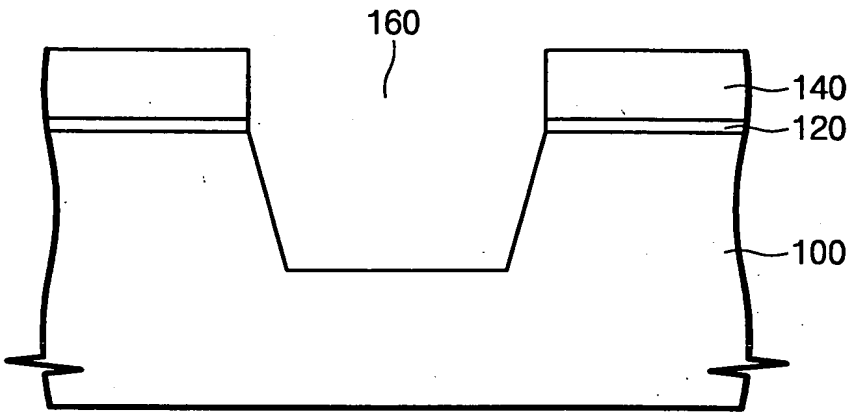
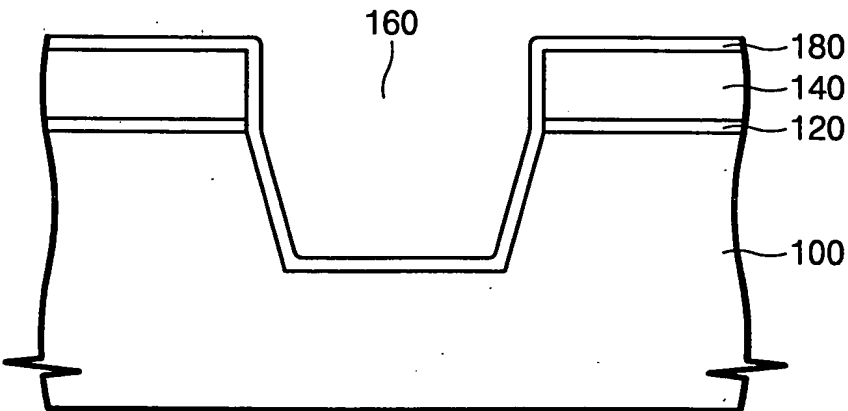


Fig. 2



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DRAFTSMAN	METHOD OF FOR	

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A DEVICE ISOLATION TRENCH IN AN INTEGRATED CIRCUIT DEVICE
 Attorney Docket No. 4591-226/Application No. Not yet assigned

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Fig. 3

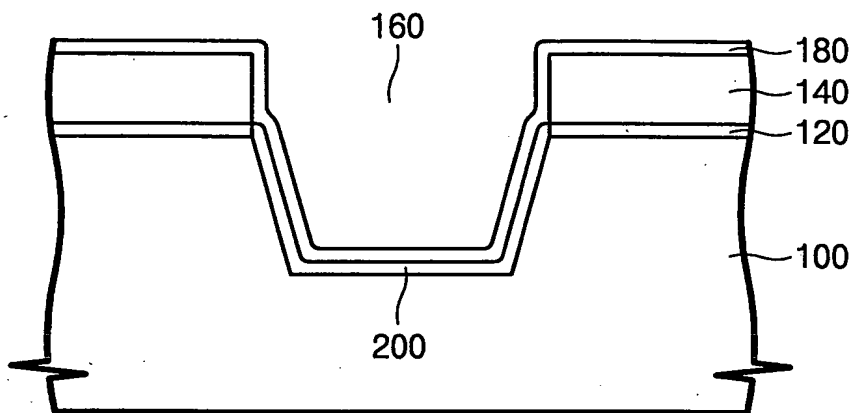
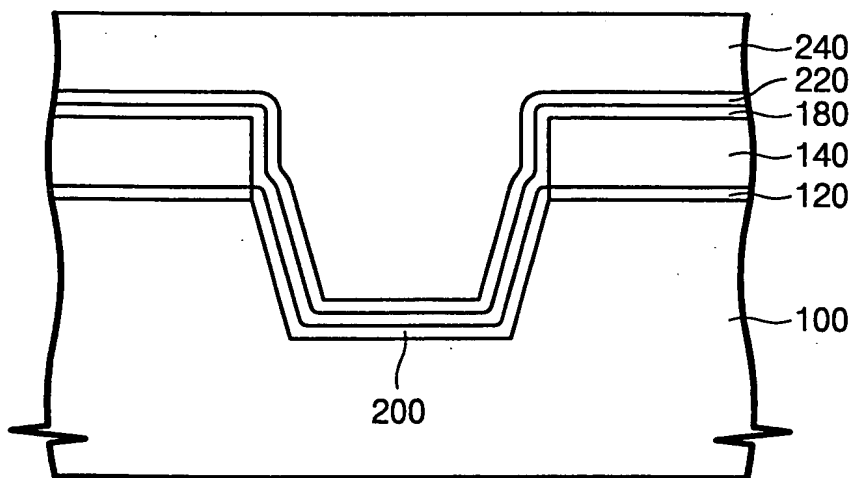


Fig. 4



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Fig. 5

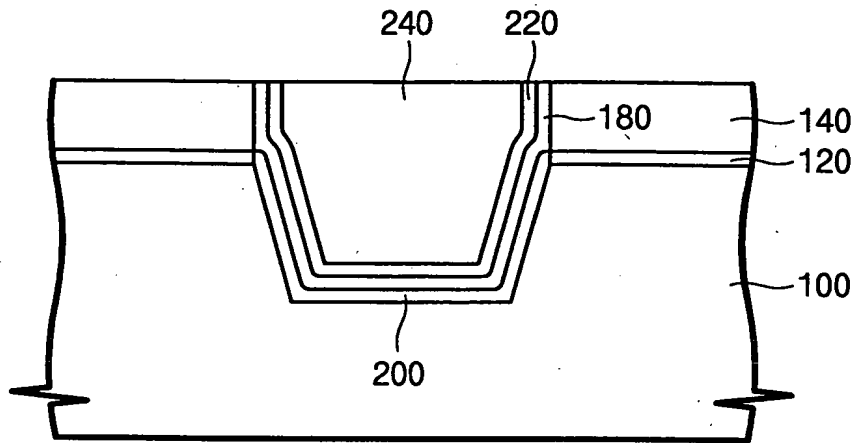


Fig. 6

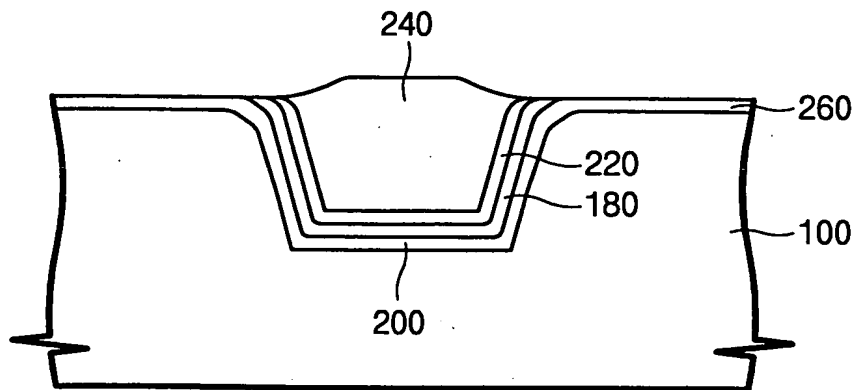


Fig. 7

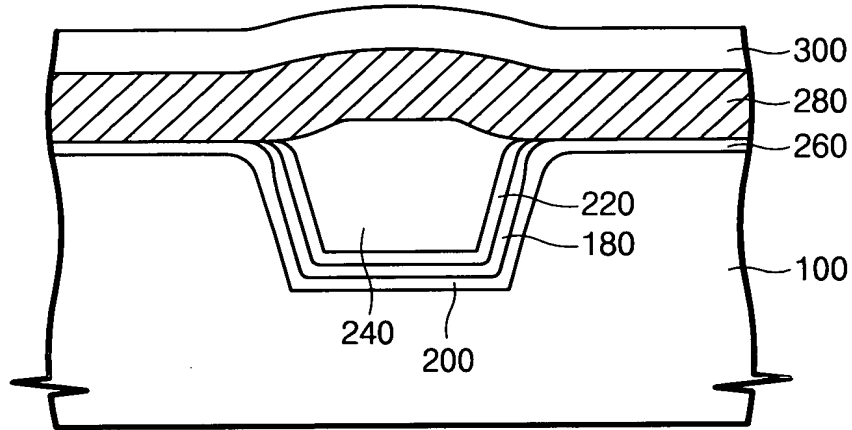
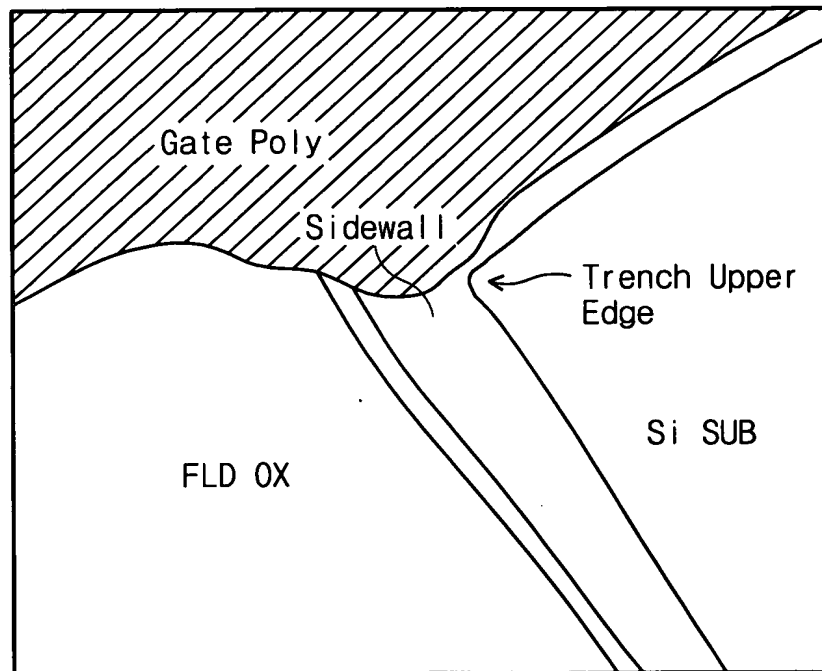


Fig. 8

(Prior Art)



APPROVED	O.G. FIG.	
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DRAFTSMAN	METHOD OF FORMING A DEVICE ISOLATION TRENCH IN AN INTEGRATED CIRCUIT DEVICE	

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 METHOD OF FORMING A DEVICE ISOLATION TRENCH IN AN INTEGRATED CIRCUIT DEVICE
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Fig. 9

(Prior Art)

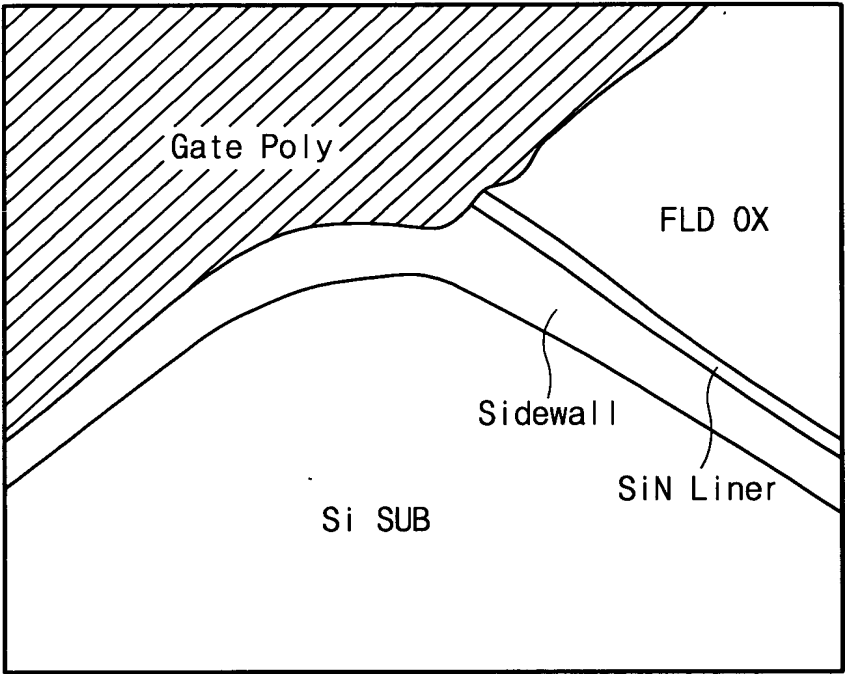


FIG. 9 is a cross-sectional view of a prior art device isolation trench structure.

Fig. 10

(Prior Art)

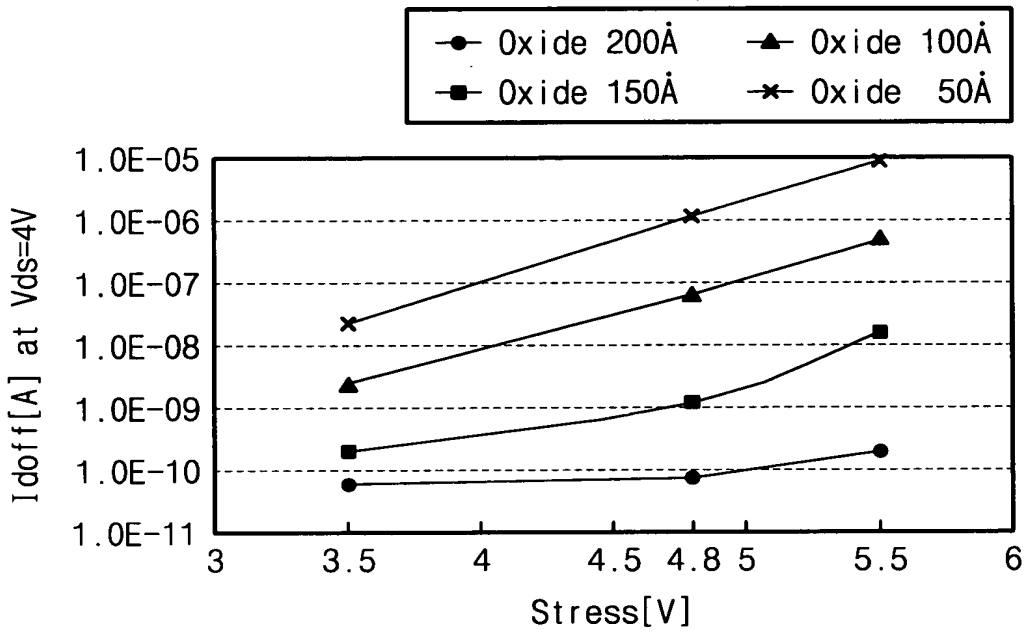


Fig. 11

